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HYBRID EVENT



IRLS: An Improved Reinforcement Learning Scheduler for High Performance Computing Systems

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Outline



- **Introduction**
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- **IRLS: An Improved Reinforcement Learning Scheduler**
 - IRLS Environment Representation
 - Scheduling Agent Rewards
- **Evaluation**
- **Conclusion and Future Work**



Scheduling on HPC Systems



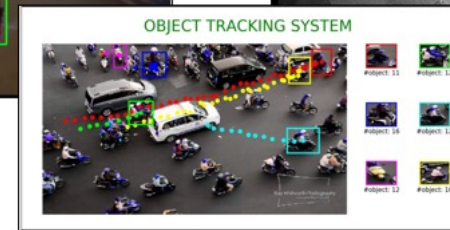
- Today, High-Performance Computing (HPC) systems are the key factor to many scientific discoveries.



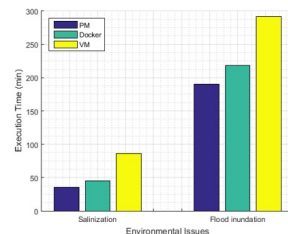
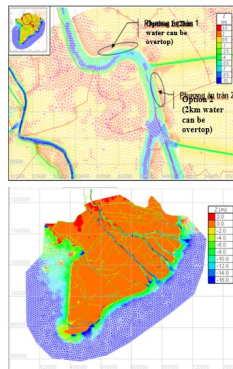
(nighttime without infrared)



(nighttime with infrared)



WTA 2018



HPC Lab

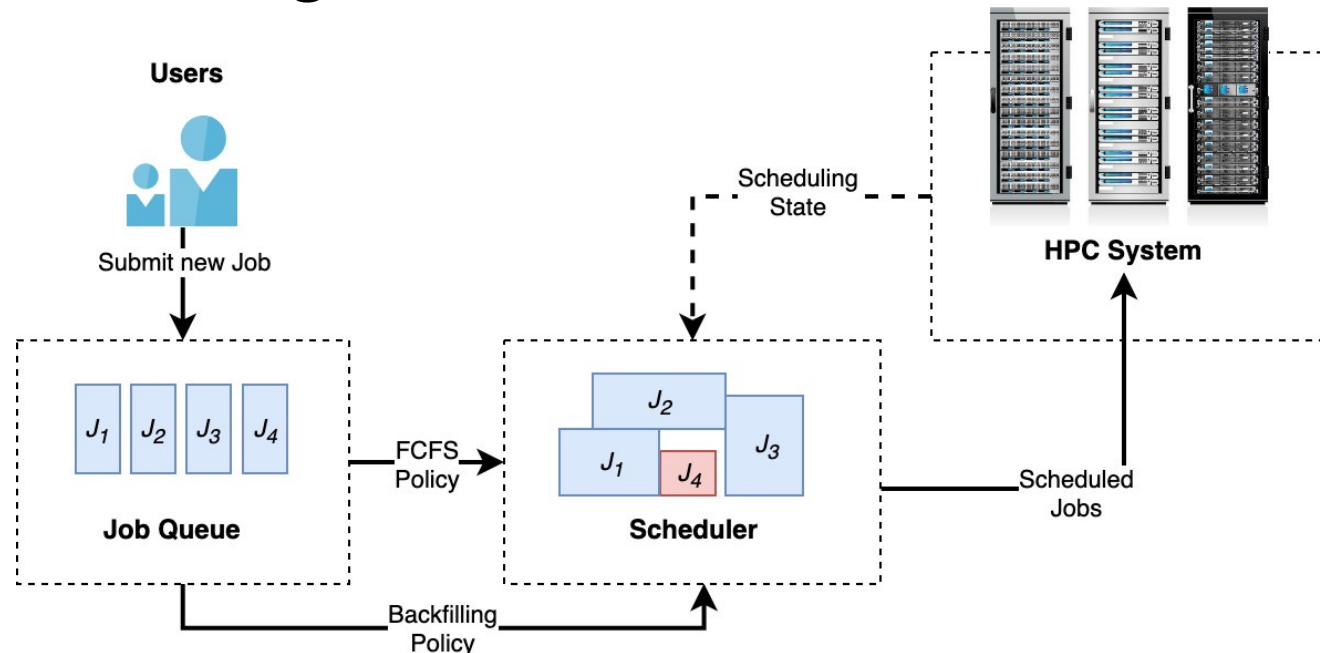


ICSSE 2023

Scheduling on HPC Systems



- Job Schedulers help ensure fair access to computing resources while maintaining optimal system utilization.
- HPC systems often use multi-level FCFS queues with Backfilling.

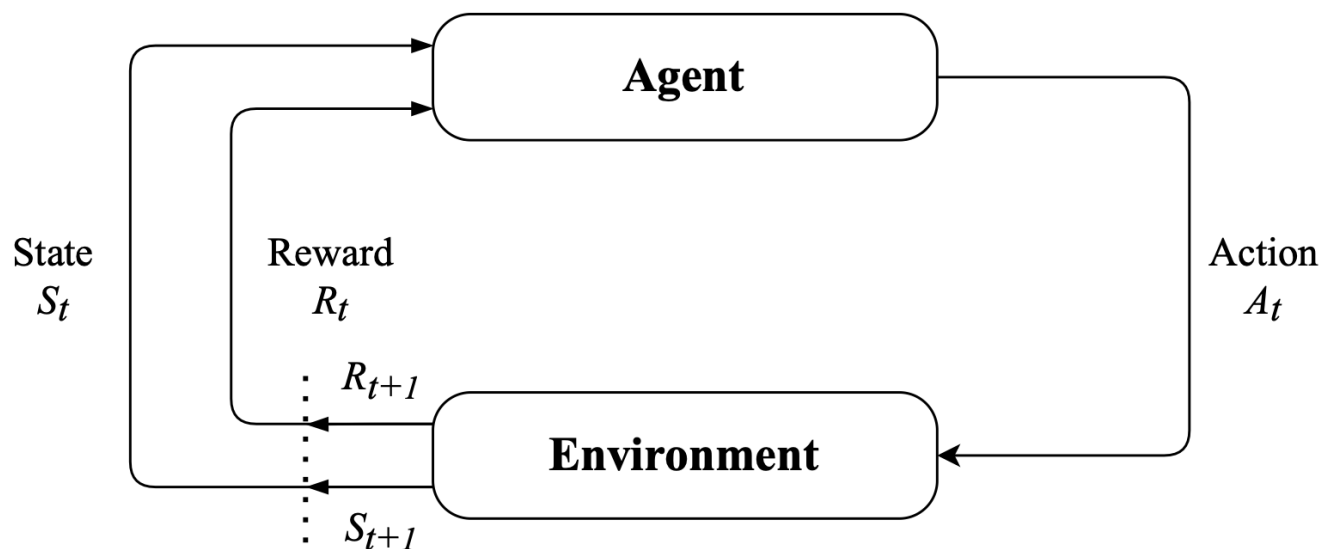




DRL Schedulers



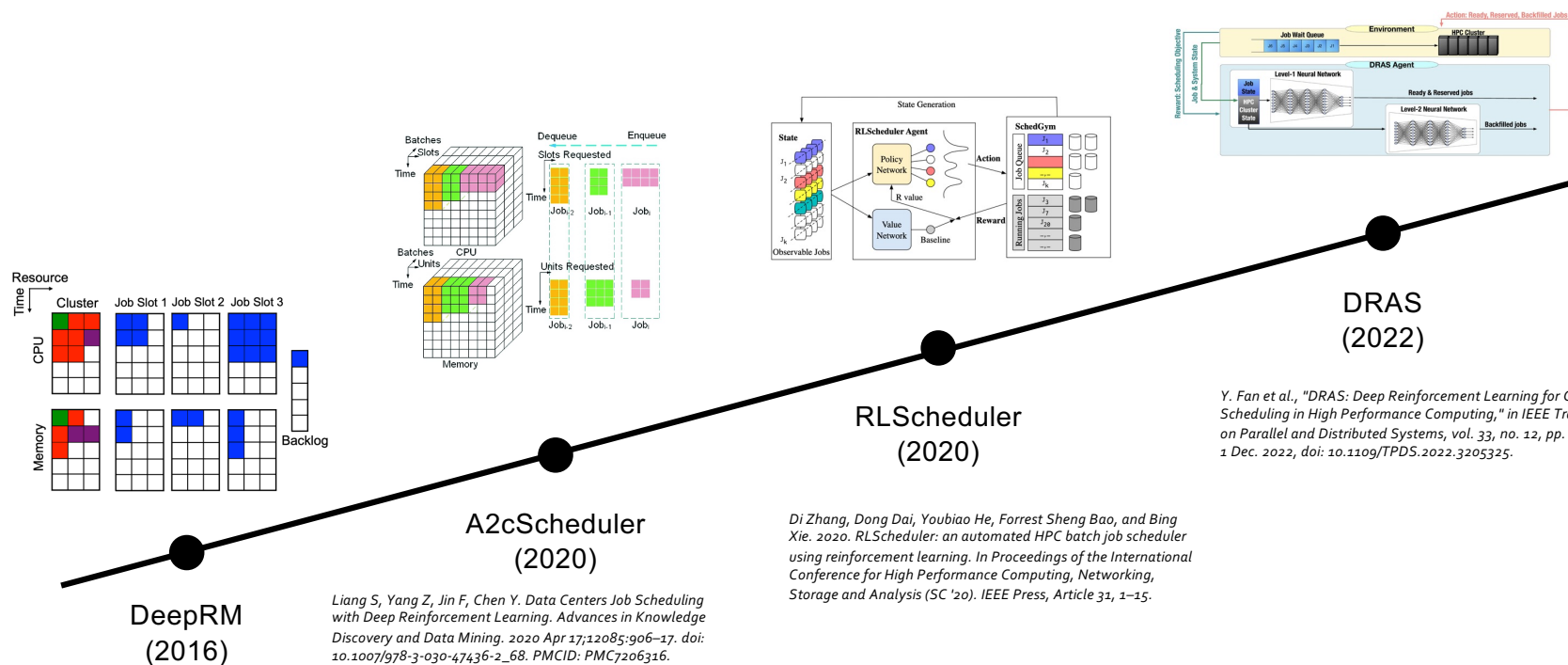
Deep Reinforcement Learning (DRL) is a type of machine learning where an agent learns to make decisions by interacting with an environment and improves by receiving rewards.



DRL Schedulers



Many **DRL Schedulers** were introduced to deal with the complexity of HPC software, hardware, and multidisciplinary research.

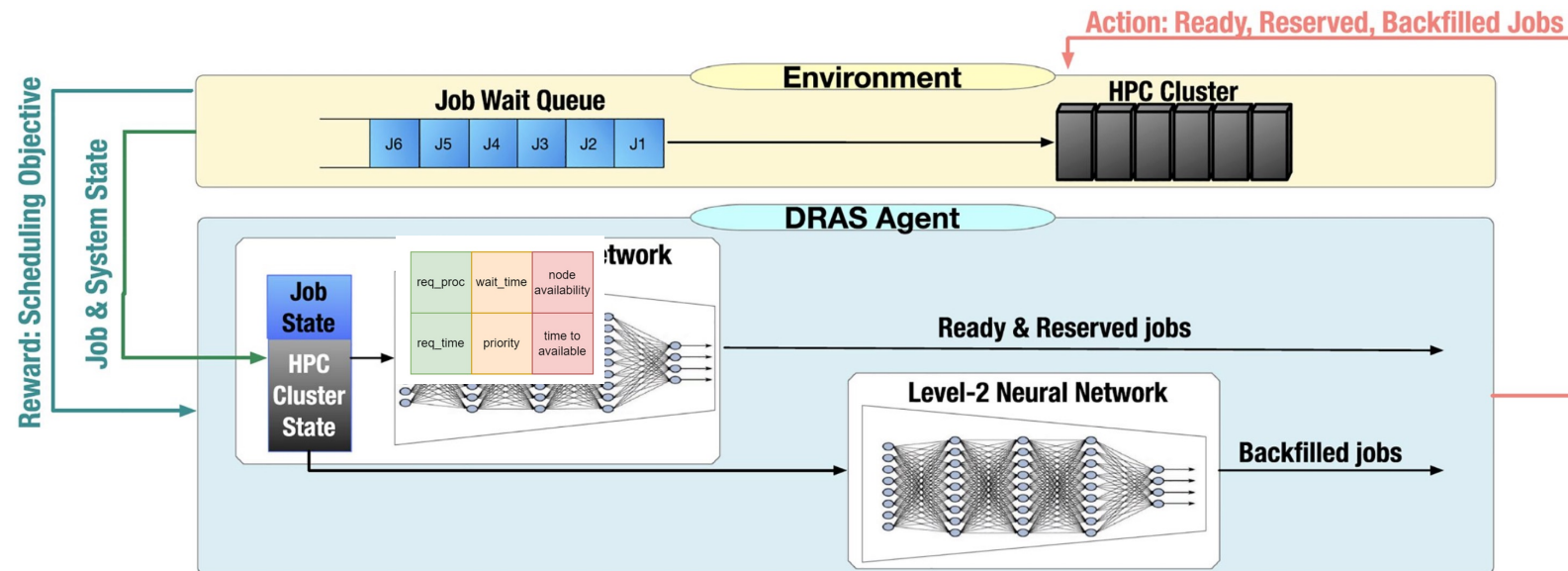




DRL Schedulers



- **DRAS (Deep Reinforcement Agent for Scheduling)** is the latest HPC scheduling model based on DRL and a hierarchical neural network.
- DRAS performs resource reservation and backfilling and efficiently learns and adapts its policy in response to workload changes.



Y. Fan, et al., "DRAS: Deep Reinforcement Learning for Cluster Scheduling in High Performance Computing" in *IEEE Transactions on Parallel & Distributed Systems*, vol. 33, no. 12, pp. 4903-4917, 2022. doi: 10.1109/TPDS.2022.3205325



DRL Schedulers



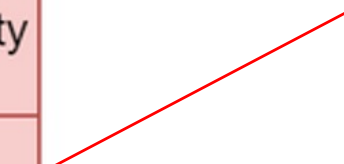
However, the HPC state representation of DRAS is simple and does not reflect the behavior in real HPC systems.

Two few attributes
for a "real" batch job



req_proc	wait_time	node availability
req_time	priority	time to available

Based on user
estimations which
is usually incorrect





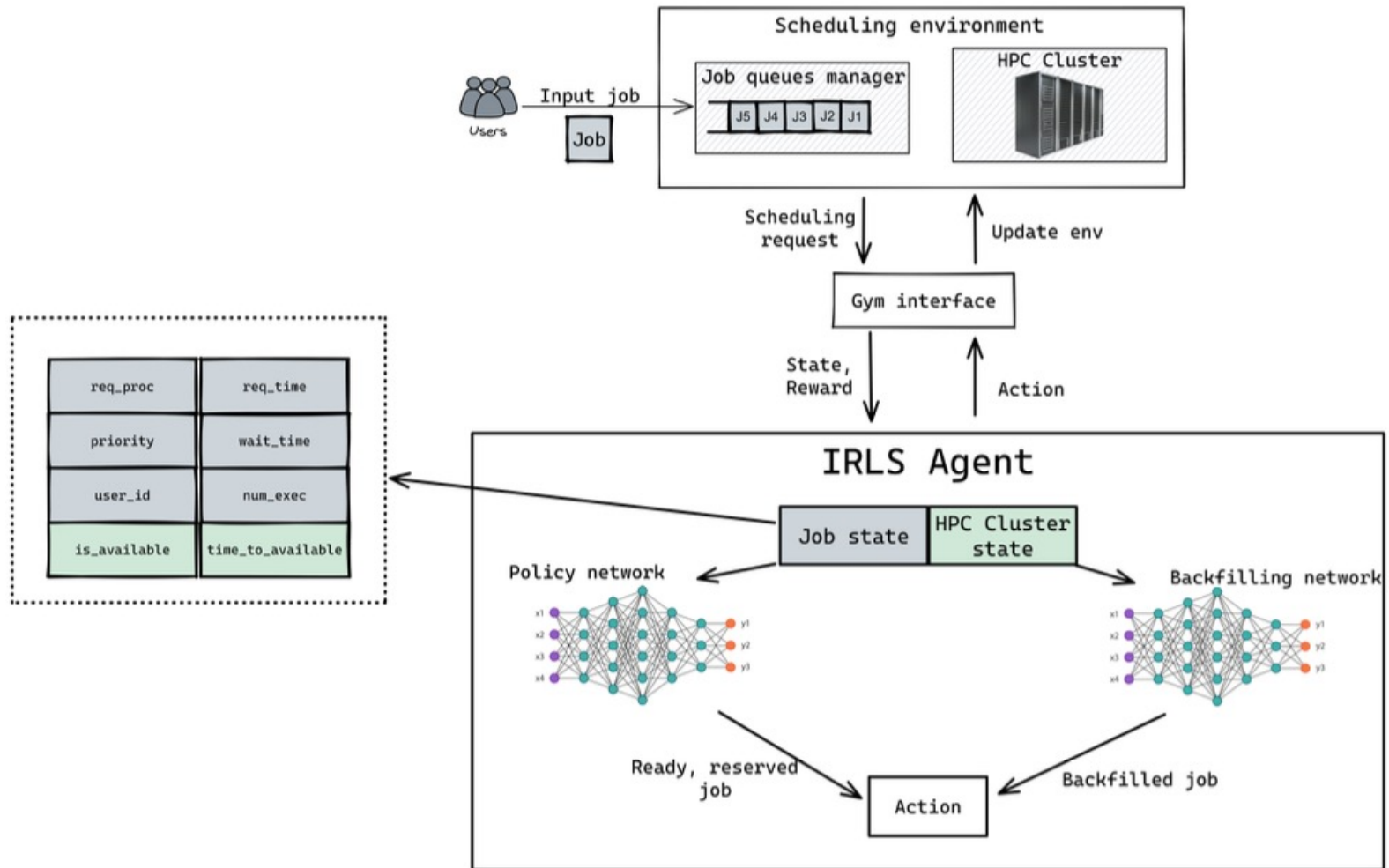
Research Goals



- Improve the HPC state representation of the DRL model with applicable attributes.
- Assist the Scheduling Agent with more accurate information from the production environment.
- Prove the possibility of the proposed solution with data from our SuperNode-XP HPC System in HCMUT, VNU-HCMC.



IRLS Overview

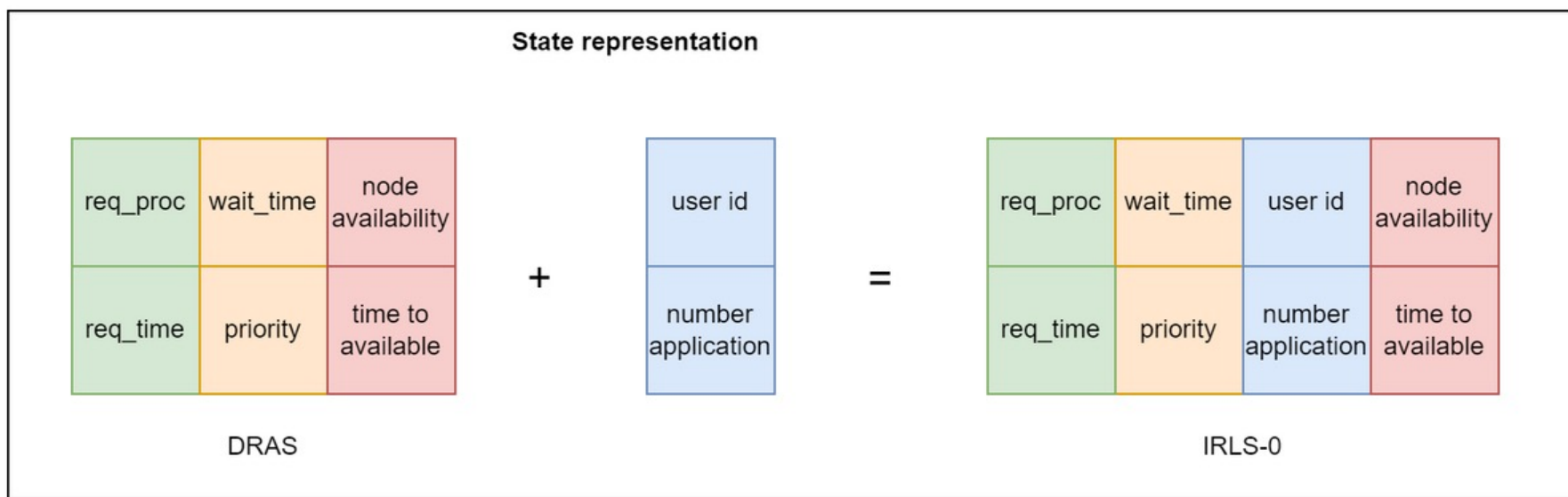




IRLS Environment Representation



Based on DRAS, we improve the state by adding two important factors: **user ID** and **application ID**, to help the agent learn from similar submitted jobs.

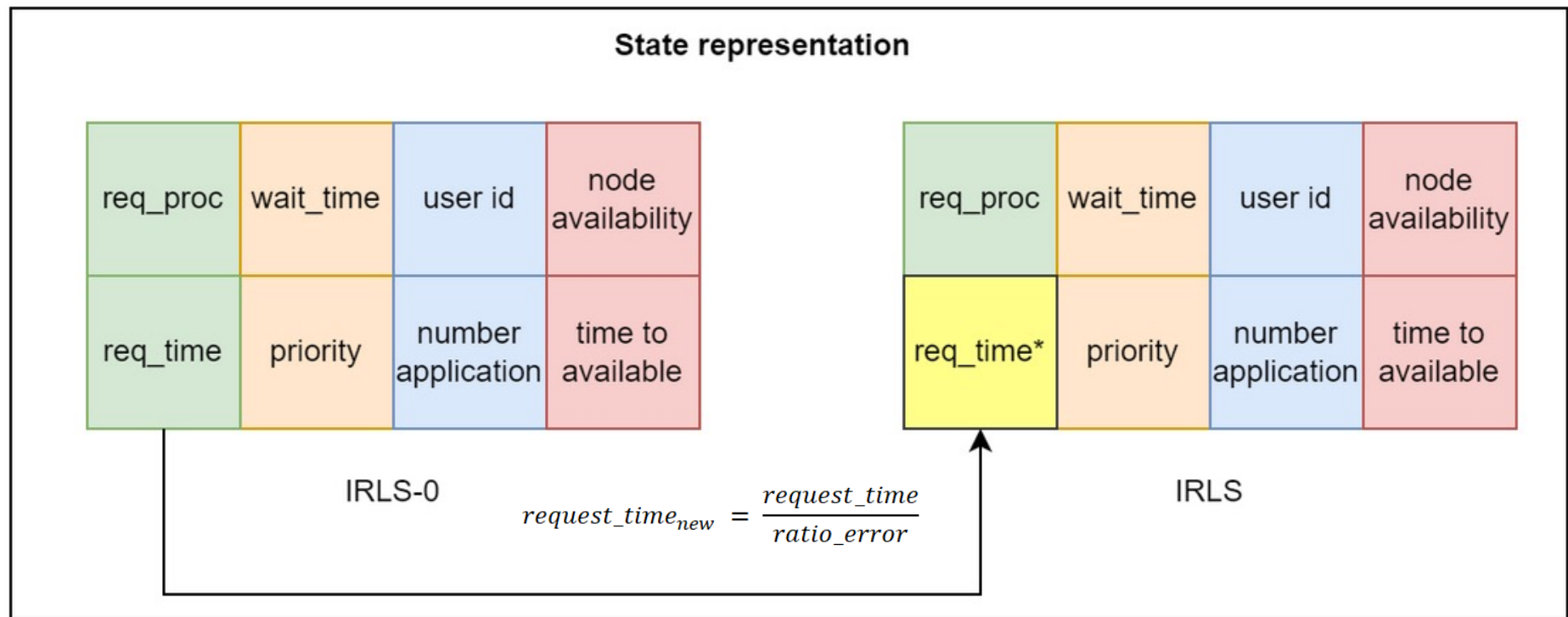




IRLS Environment Representation



- We also “**calibrate**” the user-provided **req_time** value based on their estimation history



where $ratio_error = \sum_{past} \frac{request_time}{runtime}$



Agent Scheduling Reward



- The reward function to be **maximized**:
 - Avoid the problem of job starvation
 - Focus on large-scale jobs
 - Using as many nodes of the system as possible to improve utilization.

weights based on system policy

$$w_1 * \frac{\bar{t}}{t_{max}} + w_2 * \frac{\bar{n}}{N} + w_3 * \frac{N_{used}}{N}$$

Average wait time of finished jobs Average number of requested nodes Number of allocated nodes

Max recorded wait time of finished jobs Total number of nodes in the system



Evaluation



- Experiments uses CQSim¹ on Google Colab
- Use two datasets:.

Name	<i>SDSC-SP2-1998</i>	<i>HCMUT-SuperNodeXP</i>
P (proc)	128	288
N (job)	59715	15886
\bar{i} (sec)	1055	6976
\bar{r} (sec)	17394	139933508
\bar{p} (proc)	14	48

[1] X. Yang, Z. Zhou, S. Wallace, Z. Lan, W. Tang, S. Coghlan, and M. Papka, "Integrating Dynamic Pricing of Electricity into Energy Aware Scheduling for HPC Systems", Proc. of SC'13, 2013.

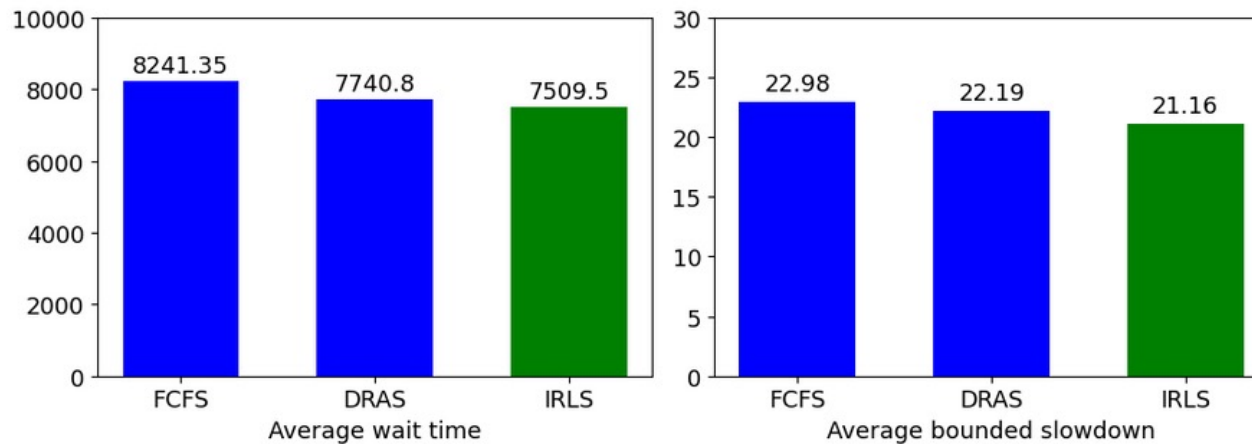


Evaluation

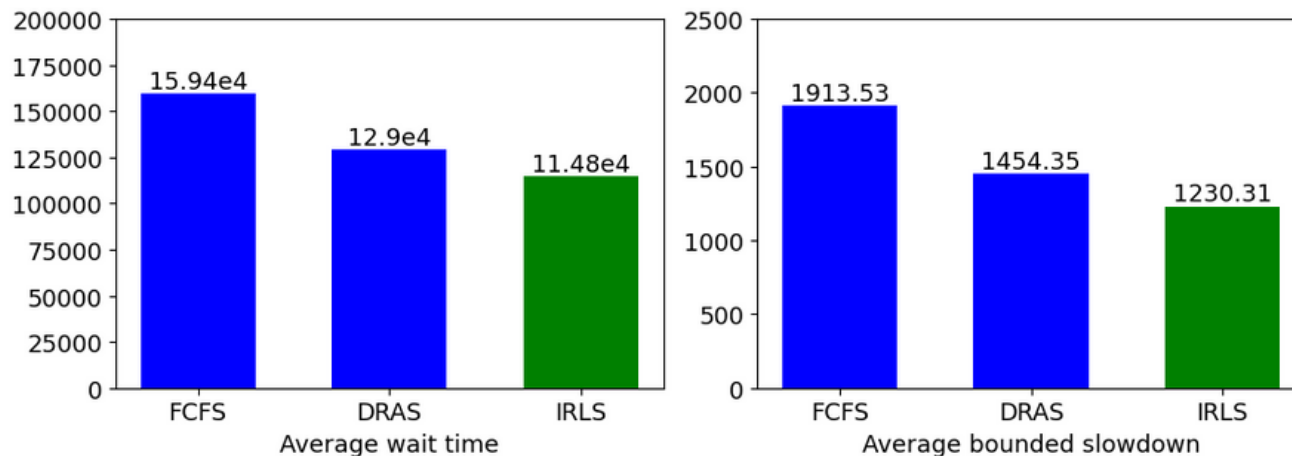


- IRLS has better scheduling performance

SDSC-SP2-1998



HCMUT-SuperNodeXP-2017





Evaluation



- IRLS still maintains an acceptable inference time

	Model			
	<i>FCFS</i>	<i>A2C</i>	<i>IRLS-0</i>	<i>IRLS</i>
AVG training time (s)	0	278	364	370
AVG inference time (s)	0	137	140	140





Conclusion and future work



- The proposed improvement effectively increases the model's performance without affecting the training and inference time, especially on our collected SuperNode-XP dataset.
- This is only the very, very first step:
 - Investigate the state representation for each particular HPC system with more constraints.
 - Extend experiments with more latest datasets from other HPC systems.





Thank you!

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